

Figure: This diagram I want to simulate in Optisystem-7

Output should be like this:-

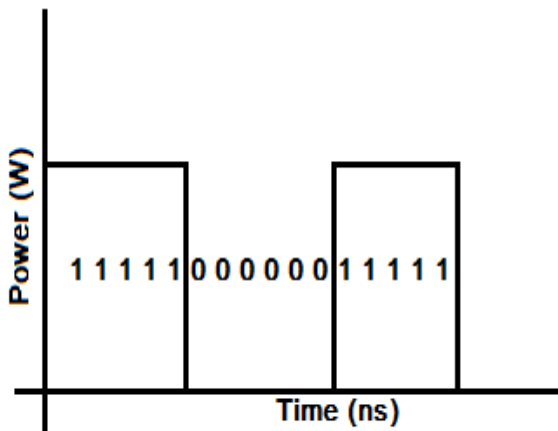


Figure: Output of Optical Time domain visualizer

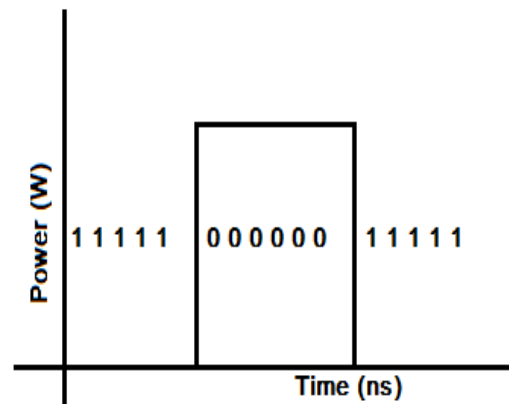


Figure: Output of Optical Time domain visualizer_1

But I have got this wired output:-

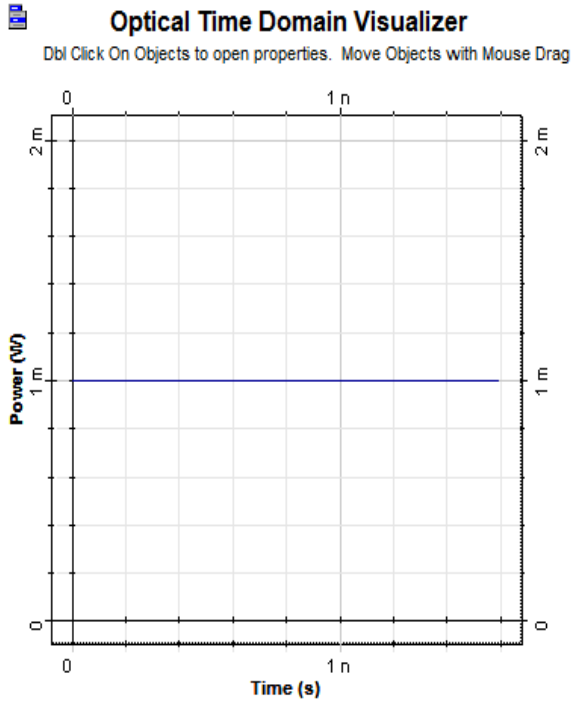


Figure: Output of Optical Time domain visualizer

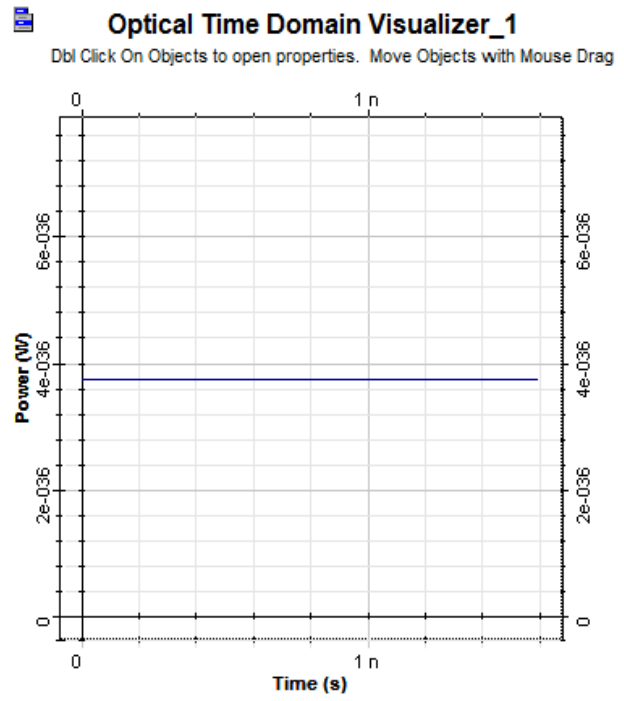


Figure: Output of Optical Time domain visualizer_1